

**POWER MANAGEMENT**

Description	Features
<p>The SC454 is a single-phase chip, high-performance PWM controller designed to power advanced IMVP-6 and IMVP6+™ processors. On-chip support is provided for all of the IMVP-6/6+ requirements, including Active Voltage Positioning, Geyserville-3 VID transitions, VID-Controlled Deeper Sleep voltage setting, PSI#, DPRSTP#, DPRSLP Control, Fast and Slow C4 Exit, and default Boot Voltage.</p>	<ul style="list-style-type: none"><li>◆ Single-Phase Solution with Integrated Drivers</li><li>◆ Hysteretic Control for Fast Transient Response</li><li>◆ VR_TT Support</li><li>◆ Active Voltage Positioning</li><li>◆ True Differential Remote (die) Sensing</li><li>◆ On-Chip Support for all IMVP-6/6+ Power Management Features</li><li>◆ VID Programmed Deeper Sleep Voltage</li><li>◆ Fast/Slow C4E Break Event Support</li><li>◆ Clock Enable (CLKEN#) Output</li><li>◆ Delayed Power Good Signal with Blanking</li><li>◆ Programmable Soft-Start and DAC Slew Control</li><li>◆ Programmable OCP Threshold</li><li>◆ Supports all Ceramic Decoupling Solutions</li><li>◆ 32-pin MLP (5x5)</li><li>◆ Latched Over-Voltage Protection</li></ul>
<p>The SC454 implements hysteretic control technology which provides the fastest possible transient response while avoiding the stability issues inherent to classical PWM controllers. Eliminating the sense resistors reduces costs and PCB area, plus increases system efficiency. Integrated SmartDriver™ technology initially turns on the high-side driver with ‘soft’ drive to reduce ringing, EMI, and capacitive turn-on of the low side MOSFET, while also increasing overall efficiency.</p>	
<p>Hysteretic operation adaptively reduces the SC454 switching frequency at light loads. Combined with an automatic “power-save” mode which prevents negative current flow in the low-side FET, system efficiency is significantly enhanced during light loading conditions.</p>	
<p>A 7-bit DAC, accurate to 0.85%, sets the output voltage reference, and implements the 0.300V to 1.500V range required by the processor. The DAC slew rate is externally programmed to minimize transient currents and audible noise. True differential remote sensing provides accurate point-of-load regulation at the processor die. Other features include programmable soft-start, open-drain IMVP-6/6+ PWRGD and CLKEN# outputs, dynamic current sharing, over-voltage and programmable over-current protection. The SC454 is available in a space-saving 32-pin MLP package.</p>	<b>Applications</b> <ul style="list-style-type: none"><li>◆ IMVP-6/6+ Notebook PCs</li><li>◆ Embedded Applications</li></ul>