

**Phase leg
Series & SiC parallel diodes
MOSFET Power Module**

V_{DSS} = 500V

R_{DSon} = 38mΩ max @ T_j = 25°C

I_D = 90A @ T_c = 25°C

Application

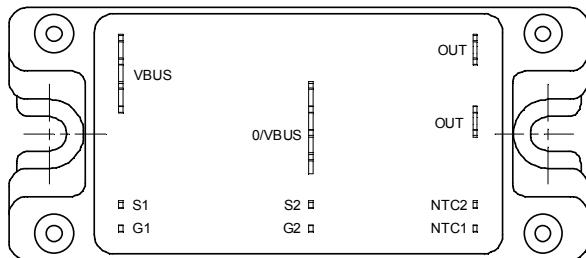
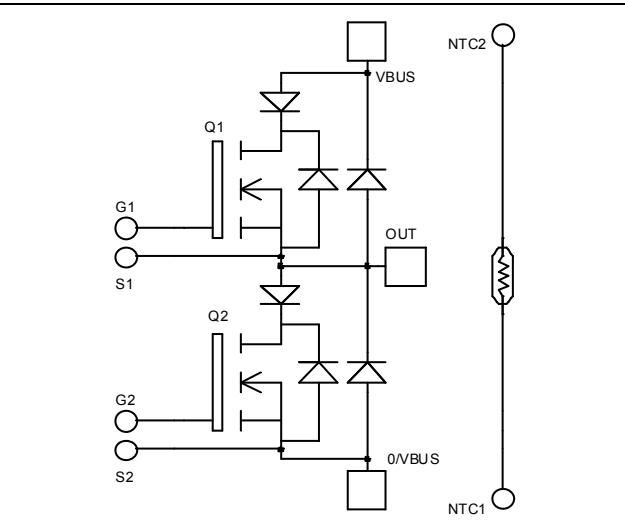
- Motor control
- Switched Mode Power Supplies
- Uninterruptible Power Supplies

Features

- **Power MOS 7® MOSFETs**
 - Low R_{DSon}
 - Low input and Miller capacitance
 - Low gate charge
 - Avalanche energy rated
- **Parallel SiC Schottky Diode**
 - Zero reverse recovery
 - Zero forward recovery
 - Temperature Independent switching behavior
 - Positive temperature coefficient on VF
- Kelvin source for easy drive
- Very low stray inductance
 - Symmetrical design
 - Lead frames for power connections
- Internal thermistor for temperature monitoring
- High level of integration

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile



Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
V _{DSS}	Drain - Source Breakdown Voltage	500	V
I _D	Continuous Drain Current	T _c = 25°C T _c = 80°C	90 67
I _{DM}	Pulsed Drain current		
V _{GS}	Gate - Source Voltage	±30	V
R _{DSon}	Drain - Source ON Resistance	38	mΩ
P _D	Maximum Power Dissipation	T _c = 25°C 694	W
I _{AR}	Avalanche current (repetitive and non repetitive)	46	A
E _{AR}	Repetitive Avalanche Energy	50	mJ
E _{AS}	Single Pulse Avalanche Energy	2500	

 **CAUTION:** These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

All ratings @ $T_j = 25^\circ\text{C}$ unless otherwise specified

Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
BV_{DSS}	Drain - Source Breakdown Voltage	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = 375\mu\text{A}$	500			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 500\text{V}$	$T_j = 25^\circ\text{C}$		150	μA
		$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 400\text{V}$	$T_j = 125^\circ\text{C}$		750	
$R_{\text{DS(on)}}$	Drain – Source on Resistance	$V_{\text{GS}} = 10\text{V}, I_{\text{D}} = 45\text{A}$			38	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}} = V_{\text{DS}}, I_{\text{D}} = 5\text{mA}$	3		5	V
I_{GSS}	Gate – Source Leakage Current	$V_{\text{GS}} = \pm 30\text{ V}, V_{\text{DS}} = 0\text{V}$			± 150	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C_{iss}	Input Capacitance	$V_{\text{GS}} = 0\text{V}$ $V_{\text{DS}} = 25\text{V}$ $f = 1\text{MHz}$		11.2		nF
C_{oss}	Output Capacitance			2.36		
C_{rss}	Reverse Transfer Capacitance			0.17		
Q_g	Total gate Charge	$V_{\text{GS}} = 10\text{V}$ $V_{\text{Bus}} = 250\text{V}$ $I_{\text{D}} = 90\text{A}$		246		nC
Q_{gs}	Gate – Source Charge			66		
Q_{gd}	Gate – Drain Charge			130		
$T_{\text{d(on)}}$	Turn-on Delay Time	Inductive switching @ 125°C $V_{\text{GS}} = 15\text{V}$ $V_{\text{Bus}} = 333\text{V}$ $I_{\text{D}} = 90\text{A}$ $R_G = 2\Omega$		18		ns
T_r	Rise Time			35		
$T_{\text{d(off)}}$	Turn-off Delay Time			87		
T_f	Fall Time			77		
E_{on}	Turn-on Switching Energy	Inductive switching @ 25°C $V_{\text{GS}} = 15\text{V}, V_{\text{Bus}} = 333\text{V}$ $I_{\text{D}} = 90\text{A}, R_G = 2\Omega$		906		μJ
E_{off}	Turn-off Switching Energy ①			1452		
E_{on}	Turn-on Switching Energy	Inductive switching @ 125°C $V_{\text{GS}} = 15\text{V}, V_{\text{Bus}} = 333\text{V}$ $I_{\text{D}} = 90\text{A}, R_G = 2\Omega$		1491		μJ
E_{off}	Turn-off Switching Energy ①			1692		

① In accordance with JEDEC standard JESD24-1.

Series diode ratings and characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$I_{\text{F(AV)}}$	Maximum Average Forward Current	50% duty cycle	$T_c = 85^\circ\text{C}$		90	A
V_F	Diode Forward Voltage	$I_F = 90\text{A}$			1.1	V
		$I_F = 180\text{A}$			1.4	
		$I_F = 90\text{A}$	$T_j = 125^\circ\text{C}$		0.9	
t_{rr}	Reverse Recovery Time	$I_F = 90\text{A}$	$T_j = 25^\circ\text{C}$		24	ns
		$V_R = 133\text{V}$ $\text{di}/\text{dt} = 600\text{A}/\mu\text{s}$	$T_j = 125^\circ\text{C}$		48	
Q_{rr}	Reverse Recovery Charge	$I_F = 90\text{A}$	$T_j = 25^\circ\text{C}$		99	nC
		$V_R = 133\text{V}$ $\text{di}/\text{dt} = 600\text{A}/\mu\text{s}$	$T_j = 125^\circ\text{C}$		450	

Parallel diode ratings and characteristics

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>		<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
I _{F(AV)}	Maximum Average Forward Current	50% duty cycle	T _c = 125°C		50		A
V _F	Diode Forward Voltage	I _F = 50A	T _j = 25°C		1.6	1.8	V
			T _j = 175°C		2.0	2.4	
Q _C	Total Capacitive Charge	I _F = 50A, V _R = 300V di/dt = 1400A/μs			70		nC
Q	Total Capacitance	f = 1MHz, V _R = 200V			325		pF
		f = 1MHz, V _R = 400V			250		

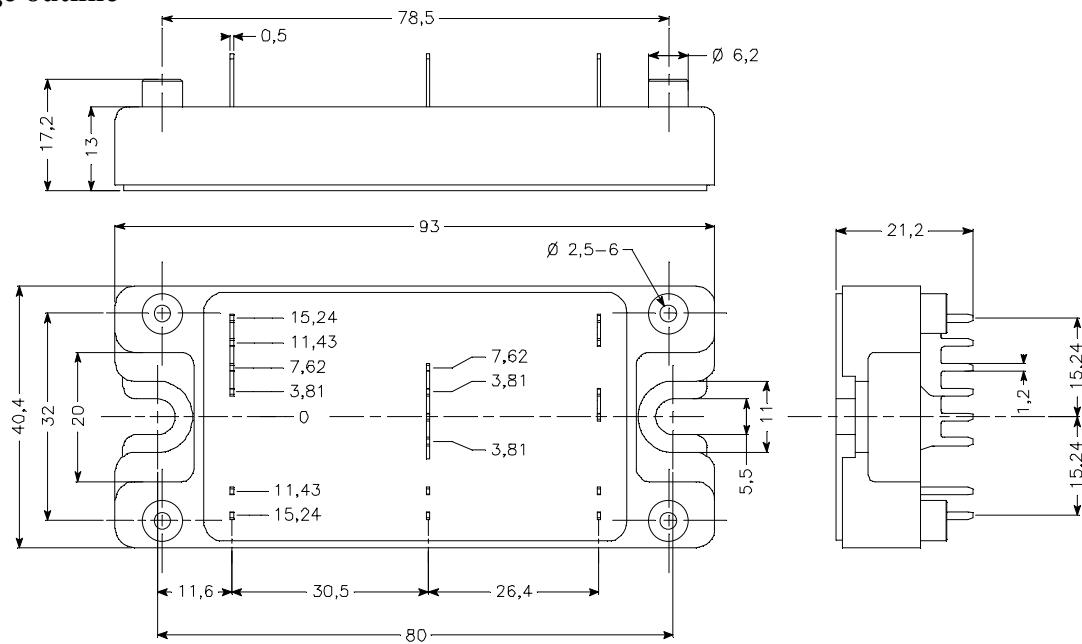
Thermal and package characteristics

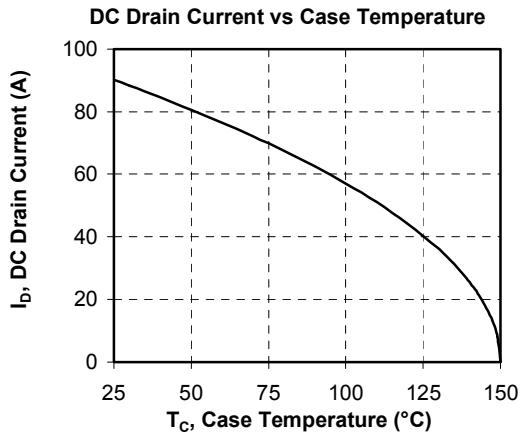
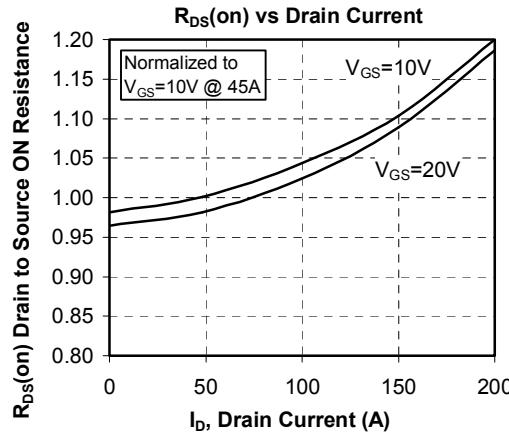
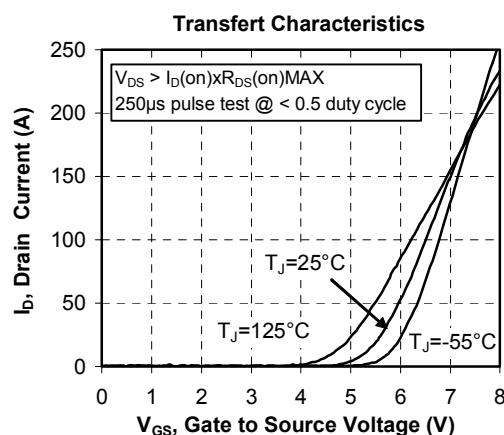
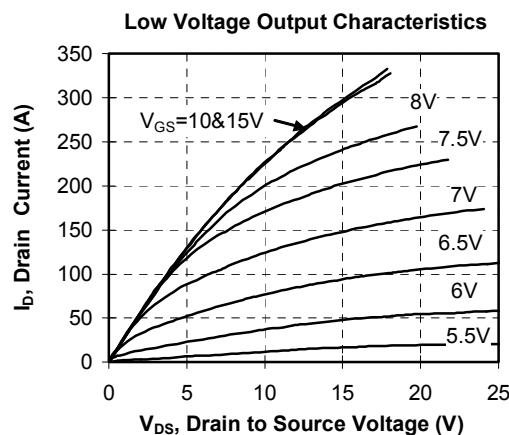
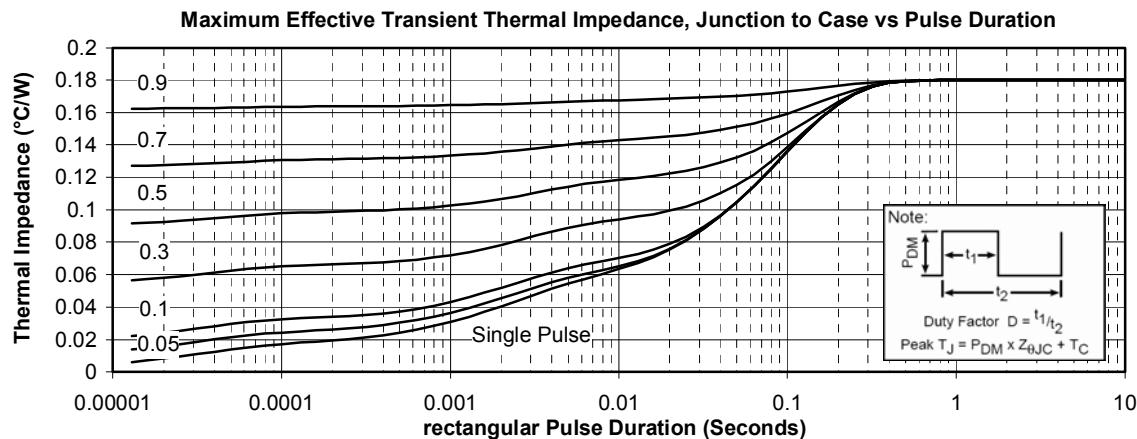
<i>Symbol</i>	<i>Characteristic</i>	<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
R _{thJC}	Junction to Case	Transistor		0.18	°C/W
		Series diode		0.45	
		Parallel diode		0.5	
V _{ISOL}	RMS Isolation Voltage, any terminal to case t=1 min, I _{isol} <1mA, 50/60Hz	2500			V
T _J	Operating junction temperature range	-40		150	°C
T _{STG}	Storage Temperature Range	-40		125	
T _C	Operating Case Temperature	-40		100	
Torque	Mounting torque	To heatsink	M5		N.m
Wt	Package Weight			160	g

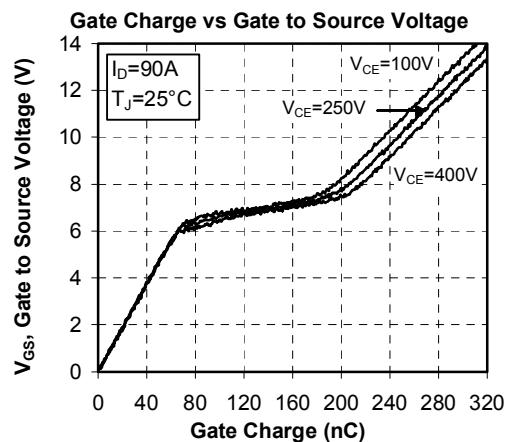
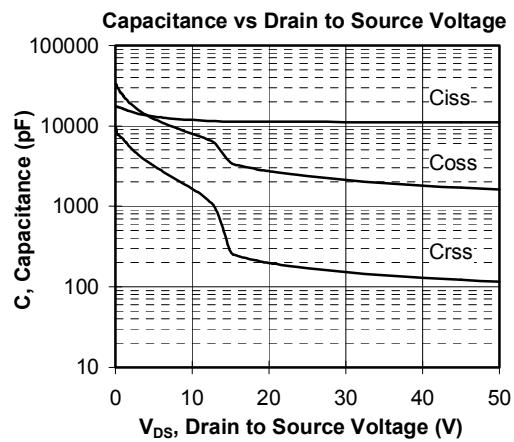
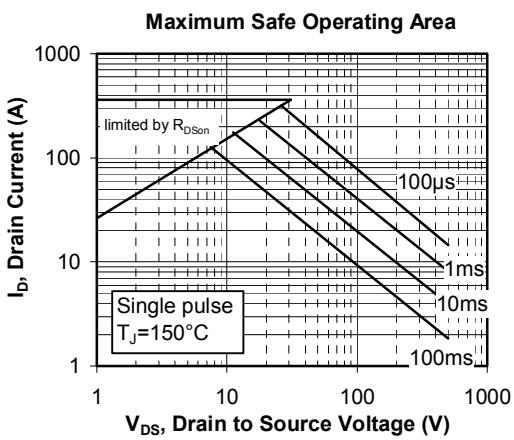
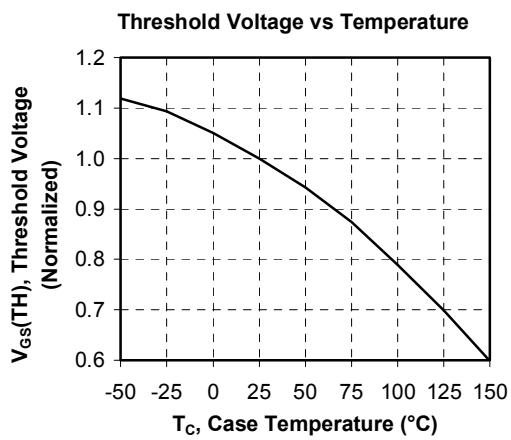
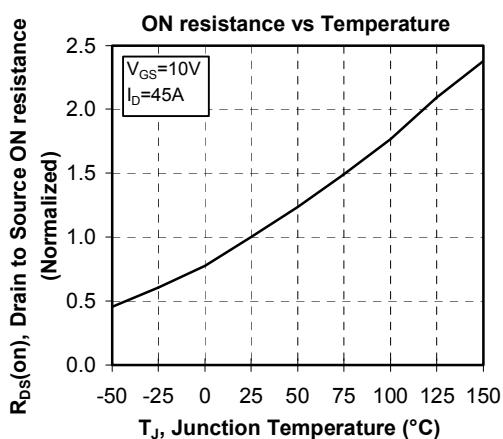
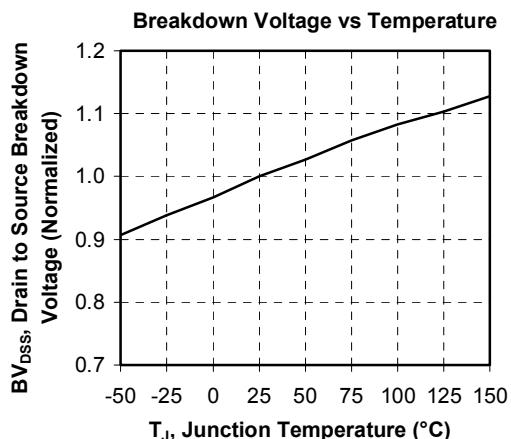
Temperature sensor NTC

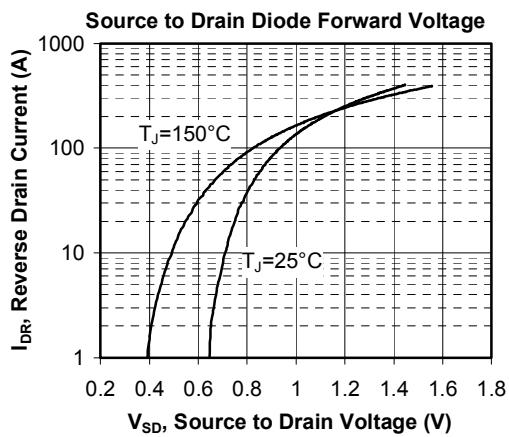
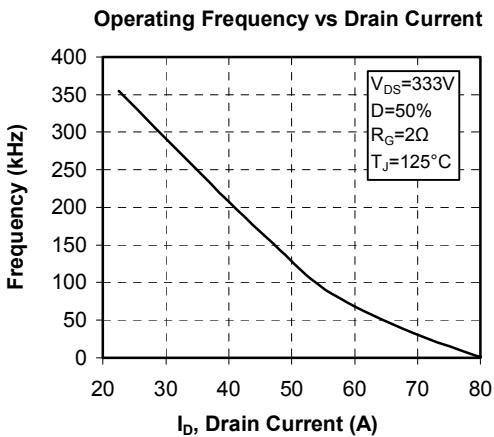
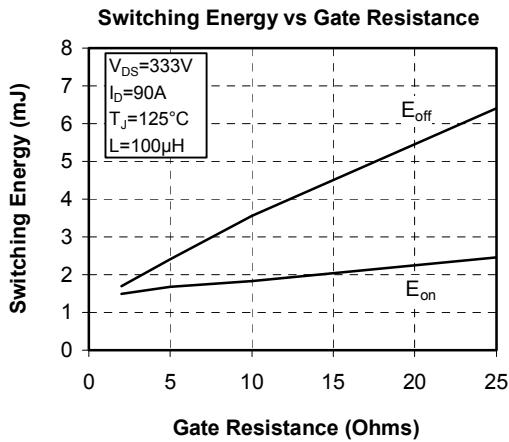
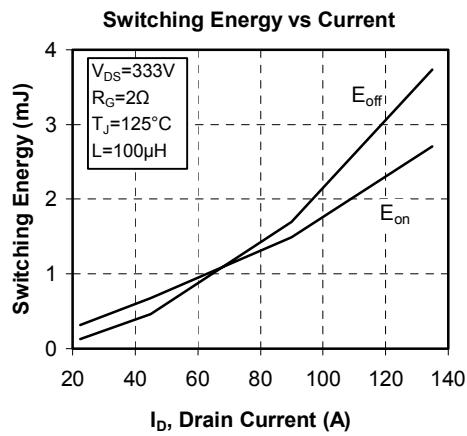
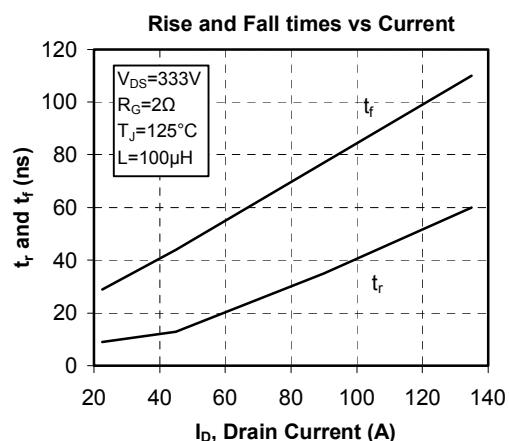
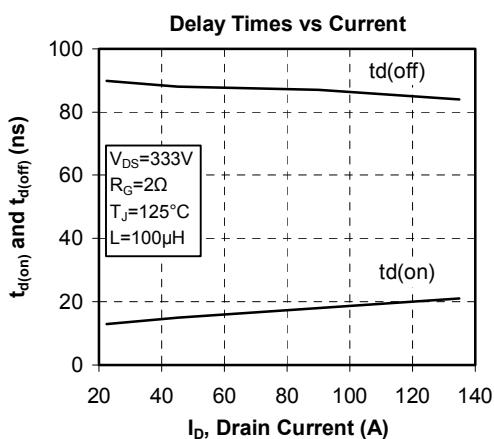
<i>Symbol</i>	<i>Characteristic</i>	<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
R ₂₅	Resistance @ 25°C		68		kΩ
B _{25/85}	T ₂₅ = 298.16 K		4080		K

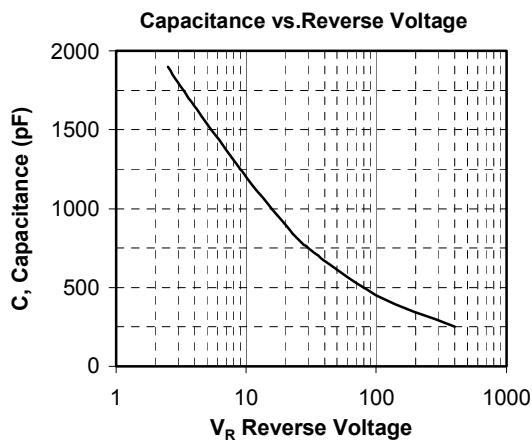
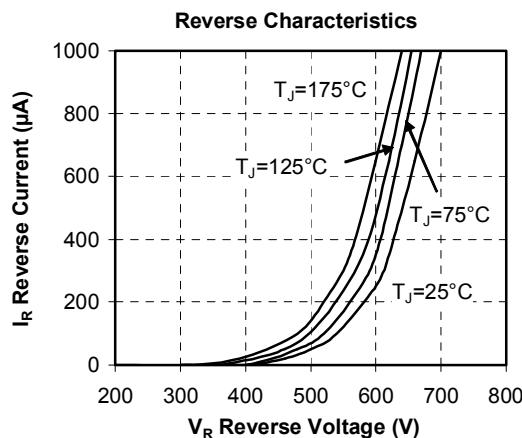
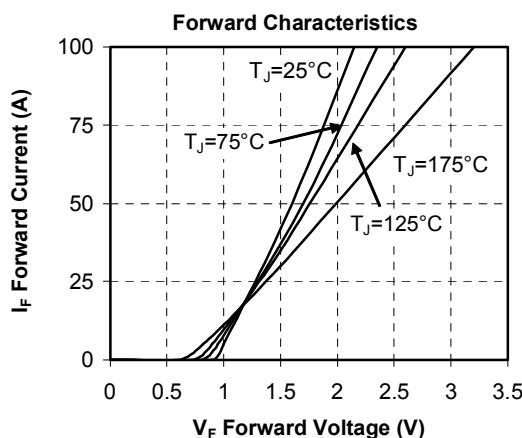
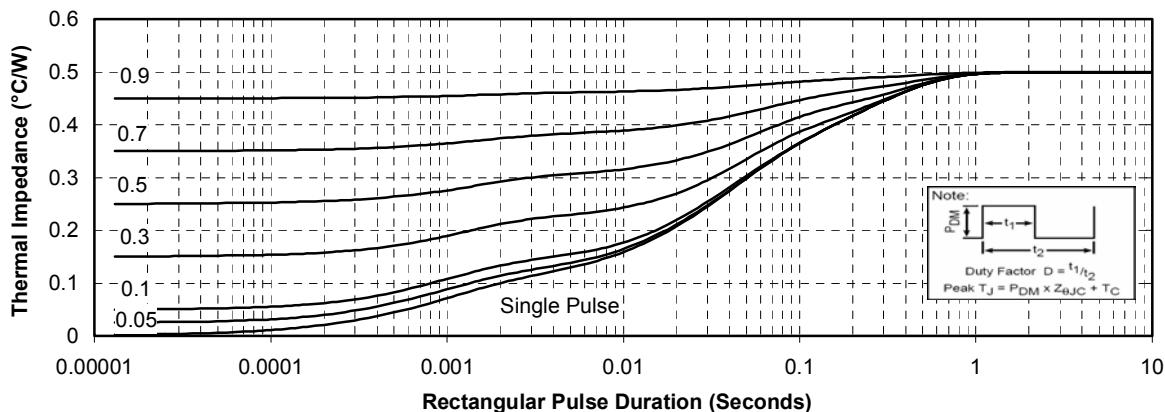
$$R_T = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]} \quad T: \text{Thermistor temperature} \\ R_T: \text{Thermistor value at } T$$

Package outline


Typical MOSFET Performance Curve






Typical SiC Diode Performance Curve
Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration


APT reserves the right to change, without notice, the specifications and information contained herein

APT's products are covered by one or more of U.S patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. U.S and Foreign patents pending. All Rights Reserved.